

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER 3

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

ABANDONED

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
097767659	D	499 419	217	2612 176	FOX

APPLICANTS
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TITLE
 Semiconductor wafer processing system with vertically-stacked process chambers and single-axis dual-wafer transfer system

PTO-2040
 12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
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